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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Eliyahou Harari et al.

Title:

Flash EEprom System With Programming Verification

Application No.:

09/129,675

Filing Date:

August 5, 1998

Examiner:

Tran, Andrew O.

Group Art Unit:

2824

Docket No.:

SNDK.006USS

Conf. No.:

4949

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or 7 7 7 6 6

Signature

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This information disclosure statement is being filed to cite documents from Investigation No. 337-TA-526 before the International Trade Commission, "Certain NAND Flash Memory Chips and Products Containing Same", and for documents related to Investigation No. 337-TA-560 before the International Trade Commission, "Certain NOR and NAND Flash Memory Chips and Products Containing Same". Investigation No. 337-TA-526, now concluded, relates to US Patent Number 5,172,338, which is a continuation in part of US patent application 07/337,579 that is incorporated into the present application in a number of locations. Investigation No. 337-TA-560 relates to US patent number 5,991,517, which is a parent of the present application.

In addition to the included briefs and prior art lists from Investigation No. 337-TA-526 that are currently being cited, the following documents are either asserted in these documents or asserted in relation to Investigation No. 337-TA-560 as relevant to the patent in suit:

US Patent No. 4,890,259 (referred to as "'259");

07/11/2006 NNGUYEN1 00000018 502664 09129675

US Patent No. 5,095,344 (referred to as "344");

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Attorney Docket No.: SNDK.006USS

Application No.: 09/129,675

US Patent No. 5,172,338 (referred to as "'338");

UK patent application GB2029145A (referred to as "GB 145 patent");

Japanese laid-open patent application no. 62-188100 (referred to as "JP100"); and a document entitled "EPM 32 Electronic Memory for 32 Station", describing the M293 product (referred to as "M293").

All of these asserted documents have either been previously cited or are being cited at this time.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

The Commissioner is hereby authorized to charge \$180.00, any additional fees, which may be required, or credit any overpayment to Deposit Account 502664.

Respectfully submitted,

Gerald P. Parsons

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Date

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Application No.: 09/129,675

Sheet 1 of 1

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT JUL 10 2006 (See several sheets if necessary) (Form PTO-1449)				SNDK.006USS			09/129,675	
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	2	Prior Art Cited by STMicro re U.S. Patent 5,172,338 – U.S. Patent and Application, 34 pages.						
3 Prior Art Cited by STMicro re U.S. Patent 5,172,338 – Foreign Patents/Applicat Sheets and Web Pages, 27 pages.							blications; D	ata
	4	Public Version of Complainant SanDisk Corporation's Reply to Respondents' Post-Hearing Brief, Investigation No. 337-TA-526, October 7, 2005, pp 81-128.						
	5	Public Version of October 7, 2005, p		Pisk Corporation's Post-	Hearing Brief	, Investigation	No. 337-TA	-526,
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